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tips for crack ies exam;Q: Supply - Supply+
regulating voltage to same circuit I have
seen there are two different circuits to
supply multiple SSI buffers as in the image
below. My question is, why do we always
supply the lowest voltage as S+ and S- even
though all the required power is not
consumed by the two buffers
simultaneously. Is there any good reason to
have the regulating done in this way? A: The
voltage of the supply can be anything you
want, provided that it can supply the output

voltages required and dissipate the power required (current I_C). For example, the supply could be 2.5 V, and the buffers could all use 1.8 V. In this case the supply must have a minimum current of:

$$\frac{V_{in}}{2.5-1.8} = \frac{V_{in}}{6.7} = \frac{4.3A}{6.7} = 0.636A$$

So in this case, each buffer's current would be: $I_C = 0.636A$ and the total current in the circuit would be: $I_{Total} = 2.7A + 2.7A = 5.4A$ A circuit that uses

higher output voltages would have to dissipate more power, and if that's not the case (i.e. all the buffers use 1.8 V output), then the power requirement of each buffer

would be: $P_{\text{Total}} = I_{\text{Total}} \times V_{\text{Total}} = 5.4 \times 1.8 = 10.8\text{W}$ Now, if there's no alternative, it's a good idea to use a regulated supply, so that you have no chance of changing the output voltage of any part of the circuit. If the voltage available is inadequate, then there's really no way to

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